



**U2**

1	PC6(PCINT14/RESET)
2	PC0(ADC0/PCINT8)
3	GND
4	PC1(ADC1/PCINT9)
5	PC2(ADC2/PCINT10)
6	AVCC
7	PC3(ADC3/PCINT11)
8	PC4(ADC4/SDA/PCINT12)
9	PC5(ADC5/SCL/PCINT13)
10	PB6(PCINT6/XTAL1/TOSC1)
11	PD0(RXD/PCINT16)
12	PB7(PCINT7/XTAL2/TOSC2)
13	PD1(TXD/PCINT17)
14	GND
15	PD2(INT0/PCINT18)
16	PD3(INT1/OC2B/PCINT19)
17	PD4(XCK/T0/PCINT20)
18	VCC
19	PD5(T1/OC0B/PCINT21)
20	PD6(AIN0/OC0A/PCINT22)
21	PD7(AIN1/PCIN23)
22	PB0(ICP1/CLKO/PCINT0)
23	PB1(OC1A/PCINT1)
24	PB2(SS/OC1B/PCINT2)
25	PB3(MOSI/OC2A/PCINT3)
26	PB4(MISO/PCINT4)
27	PB5(SCK/PCINT5)

ATmega328

**E1**

1	VDD
2	VSS
3	VO
4	RS
5	R/W*
6	E
7	DB0
8	DB1
9	DB2
10	DB3
11	DB4
12	DB5
13	DB6
14	DB7
15	A
16	K

**U3**

1	VCCA
2	GND
3	A
4	B
5	DIR
6	VCCB

74LVC1T45DBV